

L Number	Hits	Search Text	DB	Time stamp
1	84648	dram or (dynamic adj (ram or random))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:50
2	43024	sram or (static adj (ram or random))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:50
3	49415	refresh\$3 or re-fresh\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:51
4	1600	((dram or (dynamic adj (ram or random))) same (sram or (static adj (ram or random))) same (refresh\$3 or re-fresh\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:51
5	6437	((dram or (dynamic adj (ram or random))) near5 (refresh\$3 or re-fresh\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:51
6	508	((dram or (dynamic adj (ram or random))) near5 (refresh\$3 or re-fresh\$3)) with (sram or (static adj (ram or random)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:52
7	1864	(writ\$3 or stor\$3) with ((dram or (dynamic adj (ram or random))) near5 (refresh\$3 or re-fresh\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:53
8	161	(sram or (static adj (ram or random))) with ((writ\$3 or stor\$3) with ((dram or (dynamic adj (ram or random))) near5 (refresh\$3 or re-fresh\$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:59
9	6350	14.ab. or 14.clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 13:59
10	39645	11.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 14:00
11	41997	11.ab. or 11.clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 14:00
12	30005	12.ab. or 12.clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 14:00
13	11990	13.ab. or 13.clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 14:00
14	6254	(11.ab. or 11.clm.) same (12.ab. or 12.clm.) same (13.ab. or 13.clm.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/08/04 14:01

15	2154	((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab. or ((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:01
16	1854	((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:01
17	483	((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).clm.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:02
18	0	((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab.) same 12.ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:02
19	116	((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab.) same (sram or (static adj (ram or random)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:03
20	173	((sram or (static adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:03
21	68	((dram or (dynamic adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab.) and ((sram or (static adj (ram or random))) with (refresh\$3 or re-fresh\$3)).ab.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/08/04 14:03

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VLSI Circuits, 1990. Digest of Technical Papers., 1990 Symposium on , 7-9 June 1990

Page(s): 111 -112

[\[Abstract\]](#) [\[PDF Full-Text \(164 KB\)\]](#) **IEEE CNF****2 Using cache mechanisms to exploit nonrefreshing DRAMs for on-chip memories***Lee, D.D.; Katz, R.H.;*

Solid-State Circuits, IEEE Journal of , Volume: 26 Issue: 4 , April 1991

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[\[Abstract\]](#) [\[PDF Full-Text \(380 KB\)\]](#) **IEEE JNL****3 Comments on 'Using cache mechanisms to exploit nonrefreshing DRAM's for on-chip memories'***Cortadella, J.; Jove, T.;*

Solid-State Circuits, IEEE Journal of , Volume: 27 Issue: 1 , Jan. 1992

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[\[Abstract\]](#) [\[PDF Full-Text \(32 KB\)\]](#) **IEEE JNL****4 A 10 Mb frame buffer memory with Z-c mpare and A-blend units***Inoue, K.; Nakamura, H.; Kawai, H.;*

Solid-State Circuits, IEEE Journal of , Volume: 30 Issue: 12 , Dec. 1995

Page(s): 1563 -1568

5 A 12-ns 8-Mbyte DRAM secondary cache for a 64-bit microprocess r

Okuda, T.; Naritake, I.; Sugibayashi, T.; Nakajima, Y.; Murotani, T.;
Solid-State Circuits, IEEE Journal of , Volume: 35 Issue: 8 , Aug. 2000

Page(s): 1153 -1158

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) **IEEE JNL**

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Search Again**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Transparent-refresh DRAM (TRed) using dual-port DRAM cell***Sakurai, T.; Nogami, K.; Sawada, K.; Iizuka, T.;*

Custom Integrated Circuits Conference, 1988., Proceedings of the IEEE 1988, 16-19 May 1988

Page(s): 4.3/1 -4.3/4

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) **IEEE CNF****2 A 38 ns 4 Mb DRAM with a battery back-up (BBU) mode***Konishi, Y.; Dosaka, K.; Komatsu, T.; Ionue, Y.; Kumanoya, M.;**Tobita, Y.; Genjyo, H.; Nagatomo, M.; Yoshihara, T.;*

Solid-State Circuits Conference, 1990. Digest of Technical Papers.

37th ISSCC., 1990 IEEE International, 14-16 Feb. 1990

Page(s): 230 -231, 303

[\[Abstract\]](#) [\[PDF Full-Text \(668 KB\)\]](#) **IEEE CNF****3 Non-refreshing dynamic RAM for on-chip cache memories***Lee, D.D.; Katz, R.H.;*

VLSI Circuits, 1990. Digest of Technical Papers., 1990 Symposium on, 7-9 June 1990

Page(s): 111 -112

[\[Abstract\]](#) [\[PDF Full-Text \(164 KB\)\]](#) **IEEE CNF****4 A sub-0.5 μ A/MB data-retention DRAM***Yamauchi, H.; Iwata, T.; Fukushima, T.; Uno, A.; Sawada, K.;**Fukumoto, M.; Fujita, T.;*

Solid-State Circuits Conference, 1995. Digest of Technical Papers.

42nd ISSCC, 1995 IEEE International, 15-17 Feb. 1995

Page(s): 244 -245, 373

[\[Abstract\]](#) [\[PDF Full-Text \(780 KB\)\]](#) **IEEE CNF**

5 Performance improvements in high-density DRAM application using 0.15 μm body-contacted SOI technology

Jong-Wook Lee; Hyung-Ki Kim; Jong-Soo Kim; Won-Chang Lee; Jeong-Hee Oh; Dae-Gwan Kang; Yo-Hwan Koh;

SOI Conference, 2000 IEEE International , 2-5 Oct. 2000

Page(s): 76 -77

[\[Abstract\]](#) [\[PDF Full-Text \(128 KB\)\]](#) **IEEE CNF**

6 Sub-1- μA dynamic reference voltage generator for battery-operated DRAMs

Tanaka, H.; Nakagome, Y.; Etoh, J.; Yamasaki, E.; Aoki, M.; Miyazawa, K.;

Solid-State Circuits, IEEE Journal of , Volume: 29 Issue: 4 , April 1994

Page(s): 448 -453

[\[Abstract\]](#) [\[PDF Full-Text \(528 KB\)\]](#) **IEEE JNL**

7 A 10 Mb frame buffer memory with Z-compare and A-blend units

Inoue, K.; Nakamura, H.; Kawai, H.;

Solid-State Circuits, IEEE Journal of , Volume: 30 Issue: 12 , Dec. 1995

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[\[Abstract\]](#) [\[PDF Full-Text \(648 KB\)\]](#) **IEEE JNL**

8 Measurement of transient effects in SOI DRAM/SRAM access transistors

Wei, A.; Antoniadis, D.A.;

Electron Device Letters, IEEE , Volume: 17 Issue: 5 , May 1996

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